



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Group Art Unit: 2826

Application No.: 10/717,503

Examiner: T. QUACH

Filed: November 21, 2003

Docket No.: 117849

For: TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

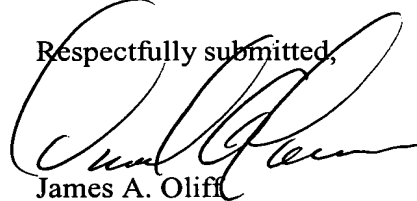
Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed with a Request for Continued Examination. No certification or fee is required.
- ☒ 2. The references were cited in a counterpart foreign application. A partial English language translation of the foreign Office Action is attached for the Examiner's information.
- ☒ 3. An English language Abstract of Reference 1 is attached hereto.
- ☒ 4. A computer-generated English language translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and is attached, but has not been reviewed for accuracy. See Reference 1.

- ☒ 5. A partial English language translation of Reference No. 2 is attached.

Respectfully submitted,



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JAO:DAT/fpw

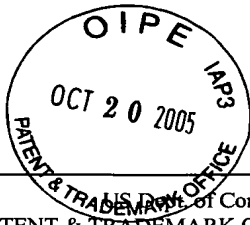
Date: October 20, 2005

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Sheet 1 of 1Form PTO-1449  
(REV. 8-83)U.S. Dept. of Commerce  
PATENT & TRADEMARK OFFICEATTY DOCKET NO.  
117849APPLICATION NO.  
10/717,503

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT(S)  
Ichio YUDASAKA et al.FILING DATE  
November 21, 2003GROUP  
2826

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
	1	JP-A 6-104439	04/15/1994	Japan		
	2	KR-A-2000-0074705	12/15/2000	Korea		

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)


EXAMINER

DATE CONSIDERED

Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date: October 20, 2005